

» Linux BSP B3001 «



Linux BSP B3001 for Intel® Core™ i7 based Platformes

- » Linux BSP designed for native compilation
- » Drivers API which is common over all Kontron Boards
- » Supports platform board specific devices
- » Driver functions accessible through/proc -filesystem
- » Supports AM4020, AM5020, CP6002 and CP3002(-RC)

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Linux BSP B3001

for Intel[®] Core[™] i7 based Platforms

The Kontron BSPs are designed to get customers started immediately with application development instead of first getting involved with BSP integration or hardware bring-up issues

Support of board specific devices and interfaces has been added to the BSP to achieve the full benefit of the functions provided by the hardware.

This BSP comes with drivers, libraries, demo applications, all it's sources and a detailed online documentation.

Supported Features	
Platforms supported	AM4020, AM5020, CP6002 and CP3002(-RC)
Supported Distribution	Verified against RedHat Fedora 13
Board Library	Common API over all supported Kontron Boards
Ethernet	10/100/1000 BASE-T Gigabit Ethernet Interface
Serial	Standard RS232 serial Ports
USB	Host Controller Interface
IPMI	Sensor Reading System Monitoring Graceful Shutdown
Integrated Devices	Hardware Watchdog (RESET, IRQ, TIMER, Dual-Stage) EEPROM for user data storage
Register access	Access to propriate hardware registers through /proc filesystem.
Mass Storage	USB Storage Devices SATA Hard Disk MM-SATA Flash Disk CF-Card (CP6002 only)
Board Status Indication	General purpose, dual color LED block
General purpose	GPIN and GPOUT support on CP3002-RC
Graphic support	Chipset integrated graphics support with 2D/3D capability
Raid support	RaidO and Raid1 possible, additional Raid5 and Raid10 on CP6002
Headless operation	Serial and Network access
TPM support	TPM 1.2 functionality
Audio	HD-Audio support on CP6002
Documentation	HTML documentation included

Note: The feature list above represents a summary of functionalities supported by the platform BSP. Depending on the used Platform Board a subset of those features is available. For more information about implemented hardware features refer to the Platform Board Hardware Manual.

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