

# » User Guide «

## **CP6004X-SA**

**6U CompactPCI Processor Board based on  
the 3<sup>rd</sup> Generation Intel® Core™ i7 Processor with  
the Intel® QM77 Express Chipset**

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## Table of Contents

<i>Revision History</i> .....	<i>ii</i>
<i>Imprint</i> .....	<i>ii</i>
<i>Disclaimer</i> .....	<i>ii</i>
<i>Table of Contents</i> .....	<i>iii</i>
<i>List of Tables</i> .....	<i>ix</i>
<i>List of Figures</i> .....	<i>xi</i>
<i>Proprietary Note</i> .....	<i>xiii</i>
<i>Trademarks</i> .....	<i>xiii</i>
<i>Environmental Protection Statement</i> .....	<i>xiii</i>
<i>Explanation of Symbols</i> .....	<i>xiv</i>
<i>For Your Safety</i> .....	<i>xv</i>
<i>High Voltage Safety Instructions</i> .....	<i>xv</i>
<i>Special Handling and Unpacking Instructions</i> .....	<i>xv</i>
<i>General Instructions on Usage</i> .....	<i>xvi</i>
<i>Two Year Warranty</i> .....	<i>xvii</i>
<b>1. Introduction</b> .....	<b>1 - 3</b>
1.1 <i>Board Overview</i> .....	1 - 3
1.2 <i>Board-Specific Information</i> .....	1 - 4
1.3 <i>System Expansion Capabilities</i> .....	1 - 5
1.3.1 <i>XMC Module</i> .....	1 - 5
1.3.2 <i>CP6004X-SA-MK2.5SATA Assembly Kit</i> .....	1 - 5
1.3.3 <i>SATA Flash Module</i> .....	1 - 5
1.3.4 <i>Rear I/O Module</i> .....	1 - 5
1.4 <i>Board Diagrams</i> .....	1 - 5
1.4.1 <i>Functional Block Diagram</i> .....	1 - 6
1.4.2 <i>Front Panel</i> .....	1 - 7
1.4.3 <i>Board Layout</i> .....	1 - 8
1.5 <i>Technical Specification</i> .....	1 - 10
1.6 <i>Standards</i> .....	1 - 16
1.7 <i>Related Publications</i> .....	1 - 17



<b>2. Functional Description .....</b>	<b>2 - 3</b>
2.1 Processor .....	2 - 3
2.2 Memory .....	2 - 4
2.3 Intel® QM77 Express Chipset .....	2 - 4
2.4 Timer .....	2 - 5
2.5 Watchdog Timer .....	2 - 5
2.6 Battery .....	2 - 5
2.7 Reset .....	2 - 5
2.8 Flash Memory .....	2 - 6
2.8.1 SPI Boot Flash for uEFI BIOS .....	2 - 6
2.8.2 SATA Flash Module (Optional) .....	2 - 6
2.9 Trusted Platform Module 1.2 (On Request) .....	2 - 6
2.10 Board Interfaces .....	2 - 7
2.10.1 Front Panel LEDs .....	2 - 7
2.10.1.1 IPMI and Hot Swap LEDs .....	2 - 7
2.10.1.2 Watchdog and Temperature Status LEDs .....	2 - 8
2.10.1.3 General Purpose LEDs .....	2 - 9
2.10.2 DIP Switches SW1 and SW2 .....	2 - 10
2.10.3 USB Interfaces .....	2 - 11
2.10.3.1 USB Connectors J6 and J7 .....	2 - 11
2.10.4 Integrated Graphics Controller .....	2 - 11
2.10.4.1 Graphics Memory Usage .....	2 - 11
2.10.4.2 Analog VGA Connector .....	2 - 12
2.10.5 COM Ports .....	2 - 13
2.10.6 Gigabit Ethernet Interfaces .....	2 - 14
2.10.7 10 Gigabit Ethernet Interfaces .....	2 - 15
2.10.8 SATA Interface .....	2 - 15
2.10.8.1 SATA Connector J14 .....	2 - 15
2.10.9 XMC Interface .....	2 - 16
2.10.10 Debug Interface .....	2 - 17
2.10.11 CompactPCI Interface .....	2 - 17
2.10.11.1 Board Functionality when Installed in System Slot .....	2 - 17
2.10.11.2 Board Functionality when Installed in Peripheral Slot .....	2 - 18
2.10.11.3 Packet Switching Backplane (PICMG 2.16) .....	2 - 18



2.10.11.4	Hot Swap Support .....	2 - 18
2.10.11.5	Power Ramping .....	2 - 18
2.10.11.6	Precharge .....	2 - 18
2.10.11.7	Handle Switch .....	2 - 18
2.10.11.8	ENUM# Interrupt .....	2 - 18
2.10.11.9	Hot Swap LED .....	2 - 18
2.10.12	CompactPCI Connectors .....	2 - 19
2.10.12.1	Connector Keying .....	2 - 19
2.10.12.2	CompactPCI Connectors J1 and J2 Pinout .....	2 - 20
2.10.12.3	CompactPCI Rear I/O Connectors J3 and J5 Pinout .....	2 - 24
2.10.12.4	High-Speed Serial Rear I/O Connectors J41 and J4 Pinout .....	2 - 27
2.10.13	High-Speed Serial Rear I/O Interconnection .....	2 - 29
<b>3.</b>	<b>Installation .....</b>	<b>3 - 3</b>
3.1	Safety Requirements .....	3 - 3
3.2	CP6004X-SA Initial Installation Procedures .....	3 - 4
3.3	Standard Removal Procedures .....	3 - 5
3.4	Hot Swap Procedures .....	3 - 6
3.4.1	System Master Hot Swap .....	3 - 6
3.4.2	Peripheral Hot Swap Procedure .....	3 - 6
3.5	Installation of CP6004X-SA Peripheral Devices .....	3 - 8
3.5.1	USB Device Installation .....	3 - 9
3.5.2	SATA Flash Module Installation .....	3 - 9
3.5.3	Installation of External SATA Devices .....	3 - 9
3.5.4	Onboard 2.5" HDD/SSD Installation .....	3 - 9
3.6	XMC Module Installation .....	3 - 9
3.6.1	Rear I/O Device Installation .....	3 - 10
3.7	Battery Replacement .....	3 - 10
3.8	Software Installation .....	3 - 11
<b>4.</b>	<b>Configuration .....</b>	<b>4 - 3</b>
4.1	DIP Switches SW1 and SW2 Configuration .....	4 - 3
4.1.1	DIP Switch SW1 Configuration .....	4 - 3



- 4.1.2 *DIP Switch SW2 Configuration* ..... 4 - 4
- 4.2 *Jumper Description* ..... 4 - 5
- 4.3 *I/O Address Map* ..... 4 - 5
- 4.4 *CP6004X-SA-Specific Registers* ..... 4 - 6
  - 4.4.1 *Status Register 0 (STAT0)* ..... 4 - 6
  - 4.4.2 *Status Register 1 (STAT1)* ..... 4 - 7
  - 4.4.3 *Control Register 0 (CTRL0)* ..... 4 - 8
  - 4.4.4 *Control Register 1 (CTRL1)* ..... 4 - 8
  - 4.4.5 *Device Protection Register (DPROT)* ..... 4 - 9
  - 4.4.6 *Reset Status Register (RSTAT)* ..... 4 - 10
  - 4.4.7 *Board Interrupt Configuration Register (BICFG)* ..... 4 - 11
  - 4.4.8 *Status Register 2 (STAT2)* ..... 4 - 12
  - 4.4.9 *Board ID High Byte Register (BIDH)* ..... 4 - 12
  - 4.4.10 *Board and PLD Revision Register (BREV)* ..... 4 - 13
  - 4.4.11 *Geographic Addressing Register (GEOAD)* ..... 4 - 13
  - 4.4.12 *Watchdog Timer Control Register (WTIM)* ..... 4 - 14
  - 4.4.13 *Board ID Low Byte Register (BIDL)* ..... 4 - 16
  - 4.4.14 *LED Configuration Register (LCFG)* ..... 4 - 17
  - 4.4.15 *LED Control Register (LCTRL)* ..... 4 - 18
  - 4.4.16 *General Purpose Output Register (GPOUT)* ..... 4 - 19
  - 4.4.17 *General Purpose Input Register (GPIN)* ..... 4 - 19
  - 4.4.18 *Status Register 3 (STAT3)* ..... 4 - 20
  - 4.4.19 *IPMI Keyboard Controller Style Interface* ..... 4 - 20

**5. Power Considerations ..... 5 - 3**

- 5.1 *System Power* ..... 5 - 3
  - 5.1.1 *CP6004X-SA Baseboard* ..... 5 - 3
  - 5.1.2 *Backplane* ..... 5 - 4
  - 5.1.3 *Power Supply Units* ..... 5 - 4
    - 5.1.3.1 *Start-Up Requirement* ..... 5 - 4
    - 5.1.3.2 *Power-Up Sequence* ..... 5 - 4
    - 5.1.3.3 *Tolerance* ..... 5 - 5
    - 5.1.3.4 *Regulation* ..... 5 - 5
- 5.2 *Power Consumption* ..... 5 - 6



5.2.1	<i>Power Consumption of the CP6004X-SA Accessories</i>	5 - 8
5.2.2	<i>Power Consumption per Gigabit Ethernet Port</i>	5 - 8
5.2.3	<i>Power Consumption per 10 Gigabit Ethernet Port</i>	5 - 8
5.3	<i>Maximum Power Consumption of XMC Modules</i>	5 - 8
<b>6.</b>	<b><i>Thermal Considerations</i></b>	<b>6 - 3</b>
6.1	<i>Board Internal Thermal Monitoring</i>	6 - 3
6.2	<i>Processor Thermal Monitoring</i>	6 - 3
6.2.1	<i>Digital Thermal Sensor (DTS)</i>	6 - 3
6.2.2	<i>Adaptive Thermal Monitor</i>	6 - 4
6.2.2.1	<i>Frequency/sVID Control</i>	6 - 4
6.2.2.2	<i>Clock Modulation</i>	6 - 4
6.2.3	<i>Catastrophic Cooling Failure Sensor</i>	6 - 5
6.3	<i>Chipset Thermal Monitor Feature</i>	6 - 5
6.4	<i>External Thermal Regulation</i>	6 - 5
6.4.1	<i>Operational Limits for the CP6004X-SA</i>	6 - 7
6.4.2	<i>Peripherals</i>	6 - 7
<b>A.</b>	<b><i>CP6004-SA-MK2.5-SATA Assembly Kit</i></b>	<b>A - 3</b>
A.1	<i>MMADP-SATA01 Module Overview</i>	A - 3
A.2	<i>Technical Specifications</i>	A - 3
A.3	<i>MMADP-SATA01 Module Layout</i>	A - 4
A.4	<i>SATA Connector J2</i>	A - 5
<b>B.</b>	<b><i>SATA Flash Module</i></b>	<b>B - 3</b>
B.1	<i>Technical Specifications</i>	B - 3
B.2	<i>SATA Flash Module Layout</i>	B - 4



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## List of Tables

1-1	<i>CP6004X-SA Main Specifications</i> .....	1 - 10
1-2	<i>Standards</i> .....	1 - 16
1-3	<i>Related Publications</i> .....	1 - 17
2-1	<i>Intel® Core™ i7-3615QE Processor Features</i> .....	2 - 4
2-2	<i>IPMI and Hot Swap LEDs Function</i> .....	2 - 7
2-3	<i>Watchdog and Temperature Status LEDs Function</i> .....	2 - 8
2-4	<i>General Purpose LEDs Function</i> .....	2 - 9
2-5	<i>POST Code Sequence</i> .....	2 - 10
2-6	<i>POST Code Example</i> .....	2 - 10
2-7	<i>DIP Switch SW1 Function</i> .....	2 - 10
2-8	<i>DIP Switch SW2 Function</i> .....	2 - 10
2-9	<i>USB Connectors J6 and J7 Pinout</i> .....	2 - 11
2-10	<i>D-Sub VGA Connector J9 Pinout</i> .....	2 - 12
2-11	<i>Serial Connector J8 (COMA) Pinout</i> .....	2 - 13
2-12	<i>Gigabit Ethernet Controller Port Mapping</i> .....	2 - 14
2-13	<i>Pinout of Gigabit Ethernet Connectors J10, J11 and J12</i> .....	2 - 14
2-14	<i>10 Gigabit Ethernet Controller Port Mapping</i> .....	2 - 15
2-15	<i>SATA Connector J14 Pinout</i> .....	2 - 15
2-16	<i>XMC Connector J20 Pinout</i> .....	2 - 16
2-17	<i>CompactPCI PCI/PCI-X Configuration</i> .....	2 - 17
2-18	<i>CompactPCI Bus Connector J1 System Slot Pinout</i> .....	2 - 20
2-19	<i>CompactPCI Bus Connector J1 Peripheral Slot Pinout</i> .....	2 - 21
2-20	<i>64-bit CompactPCI Bus Connector J2 System Slot Pinout</i> .....	2 - 22
2-21	<i>64-bit CompactPCI Bus Connector J2 Peripheral Slot Pinout</i> .....	2 - 23
2-22	<i>CompactPCI Rear I/O Connector J3 Pinout</i> .....	2 - 24
2-23	<i>CompactPCI Rear I/O Connector J3 Signals</i> .....	2 - 25
2-25	<i>CompactPCI Rear I/O Connector J5 Signals</i> .....	2 - 26
2-24	<i>CompactPCI Rear I/O Connector J5 Pinout</i> .....	2 - 26
2-26	<i>High-Speed Serial Rear I/O Connector J41 Pinout</i> .....	2 - 27
2-27	<i>High-Speed Serial Rear I/O Connector J4 Pinout</i> .....	2 - 28
2-28	<i>High-Speed Serial Rear I/O Connectors J41 and J4 Signal Description..</i>	2 - 28
2-29	<i>High-Speed Serial Rear I/O Interconnection Port Mapping</i> .....	2 - 29
4-1	<i>DIP Switch SW1 for Boot Configuration</i> .....	4 - 3



4-2	<i>DIP Switch SW2 for CompactPCI Interface Configuration</i> .....	4 - 4
4-3	<i>I/O Address Map</i> .....	4 - 5
4-4	<i>Status Register 0 (STAT0)</i> .....	4 - 6
4-5	<i>Status Register 1 (STAT1)</i> .....	4 - 7
4-6	<i>Control Register 0 (CTRL0)</i> .....	4 - 8
4-7	<i>Control Register 1 (CTRL1)</i> .....	4 - 8
4-8	<i>Device Protection Register (DPROT)</i> .....	4 - 9
4-9	<i>Reset Status Register (RSTAT)</i> .....	4 - 10
4-10	<i>Board Interrupt Configuration Register (BICFG)</i> .....	4 - 11
4-11	<i>Status Register 2 (STAT2)</i> .....	4 - 12
4-12	<i>Board ID High Byte Register (BIDH)</i> .....	4 - 12
4-13	<i>Board and PLD Revision Register (BREV)</i> .....	4 - 13
4-14	<i>Geographic Addressing Register (GEOAD)</i> .....	4 - 13
4-15	<i>Watchdog Timer Control Register (WTIM)</i> .....	4 - 15
4-16	<i>Board ID Low Byte Register (BIDL)</i> .....	4 - 16
4-17	<i>LED Configuration Register (LCFG)</i> .....	4 - 17
4-18	<i>LED Control Register (LCTRL)</i> .....	4 - 18
4-19	<i>General Purpose Output Register (GPOUT)</i> .....	4 - 19
4-20	<i>General Purpose Input Register (GPIN)</i> .....	4 - 19
4-21	<i>Status Register 3 (STAT3)</i> .....	4 - 20
5-1	<i>Maximum Input Power Voltage Limits</i> .....	5 - 3
5-2	<i>DC Operational Input Voltage Ranges</i> .....	5 - 3
5-3	<i>Input Voltage Characteristics</i> .....	5 - 5
5-4	<i>CP6004X-SA in uEFI Shell Mode</i> .....	5 - 7
5-5	<i>CP6004X-SA with Win. 7 and Processor and Graphics in Idle State</i> .....	5 - 7
5-6	<i>CP6004X-SA with Win. 7 and Max. Proc. Workload and Basic Graphics Oper.</i>	5 - 7
5-7	<i>CP6004X-SA with Win. 7 and Max. Processor and Graphics Workload ....</i>	5 - 7
5-8	<i>Power Consumption of CP6004X-SA Accessories</i> .....	5 - 8
5-9	<i>Power Consumption per Gigabit Ethernet Port</i> .....	5 - 8
5-10	<i>Power Consumption per 10 Gigabit Ethernet Port</i> .....	5 - 8
5-11	<i>XMC Module Current</i> .....	5 - 8
A-1	<i>MMADP-SATA01 Main Specifications</i> .....	A - 3
A-2	<i>SATA Connector J2 Pinout</i> .....	A - 5
B-1	<i>SATA Flash Module Main Specifications</i> .....	B - 3





## List of Figures

1-1	<i>CP6004X-SA Functional Block Diagram</i> .....	1 - 6
1-2	<i>CP6004X-SA Front Panel</i> .....	1 - 7
1-3	<i>CP6004X-SA Board Layout – Top View</i> .....	1 - 8
1-4	<i>CP6004X-SA Board Layout – Bottom View</i> .....	1 - 9
2-1	<i>USB Connectors J6 and J7</i> .....	2 - 11
2-2	<i>D-Sub VGA Connector J9</i> .....	2 - 12
2-3	<i>Serial Connector J8 (COMA)</i> .....	2 - 13
2-4	<i>Gigabit Ethernet Connectors J10, J11 and J12</i> .....	2 - 14
2-5	<i>SATA Connector J14</i> .....	2 - 15
2-6	<i>XMC Connector J20</i> .....	2 - 16
2-7	<i>CompactPCI Connectors</i> .....	2 - 19
3-1	<i>Connecting a Peripheral Device to the CP6004X-SA</i> .....	3 - 8
4-1	<i>DIP Switch SW1</i> .....	4 - 3
4-2	<i>DIP Switch SW2</i> .....	4 - 4
6-1	<i>CP6004X-SA with Core™ i7-3615QE (SV) 2.3 GHz</i> .....	6 - 7
A-1	<i>MMADP-SATA01 Module Layout</i> .....	A - 4
A-2	<i>SATA Connector J2</i> .....	A - 5
B-1	<i>SATA Flash Module Layout (Bottom View)</i> .....	B - 4



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This product has been manufactured to satisfy environmental protection requirements where possible. Many of the components used (structural parts, printed circuit boards, connectors, batteries, etc.) are capable of being recycled.

Final disposition of this product after its service life must be accomplished in accordance with applicable country, state, or local laws or regulations.



## Explanation of Symbols



### ***Caution, Electric Shock!***

This symbol and title warn of hazards due to electrical shocks (> 60V) when touching products or parts of them. Failure to observe the precautions indicated and/or prescribed by the law may endanger your life/health and/or result in damage to your material.

Please refer also to the section “High Voltage Safety Instructions” on the following page.



### ***Warning, ESD Sensitive Device!***

This symbol and title inform that electronic boards and their components are sensitive to static electricity. Therefore, care must be taken during all handling operations and inspections of this product, in order to ensure product integrity at all times.

Please read also the section “Special Handling and Unpacking Instructions” on the following page.



### ***Warning!***

This symbol and title emphasize points which, if not fully understood and taken into consideration by the reader, may endanger your health and/or result in damage to your material.



### ***Note ...***

This symbol and title emphasize aspects the reader should read through carefully for his or her own advantage.



## For Your Safety

Your new Kontron product was developed and tested carefully to provide all features necessary to ensure its compliance with electrical safety requirements. It was also designed for a long fault-free life. However, the life expectancy of your product can be drastically reduced by improper treatment during unpacking and installation. Therefore, in the interest of your own safety and of the correct operation of your new Kontron product, you are requested to conform with the following guidelines.

### High Voltage Safety Instructions



#### **Warning!**

All operations on this device must be carried out by sufficiently skilled personnel only.



#### **Caution, Electric Shock!**

Before installing a not hot-swappable Kontron product into a system always ensure that your mains power is switched off. This applies also to the installation of piggybacks.

Serious electrical shock hazards can exist during all installation, repair and maintenance operations with this product. Therefore, always unplug the power cable and any other cables which provide external voltages before performing work.

### Special Handling and Unpacking Instructions



#### **ESD Sensitive Device!**

Electronic boards and their components are sensitive to static electricity. Therefore, care must be taken during all handling operations and inspections of this product, in order to ensure product integrity at all times.

Do not handle this product out of its protective enclosure while it is not used for operational purposes unless it is otherwise protected.

Whenever possible, unpack or pack this product only at EOS/ESD safe work stations. Where a safe work station is not guaranteed, it is important for the user to be electrically discharged before touching the product with his/her hands or tools. This is most easily done by touching a metal part of your system housing.

It is particularly important to observe standard anti-static precautions when changing piggybacks, ROM devices, jumper settings etc. If the product contains batteries for RTC or memory backup, ensure that the board is not placed on conductive surfaces, including anti-static plastics or sponges. They can cause short circuits and damage the batteries or conductive circuits on the board.



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## General Instructions on Usage

In order to maintain Kontron's product warranty, this product must not be altered or modified in any way. Changes or modifications to the device, which are not explicitly approved by Kontron and described in this manual or received from Kontron's Technical Support as a special handling instruction, will void your warranty.

This device should only be installed in or connected to systems that fulfill all necessary technical and specific environmental requirements. This applies also to the operational temperature range of the specific board version, which must not be exceeded. If batteries are present, their temperature restrictions must be taken into account.

In performing all necessary installation and application operations, please follow only the instructions supplied by the present manual.

Keep all the original packaging material for future storage or warranty shipments. If it is necessary to store or ship the board, please re-pack it as nearly as possible in the manner in which it was delivered.

Special care is necessary when handling or unpacking the product. Please consult the special handling and unpacking instruction on the previous page of this manual.





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If the customer's eligibility for warranty has not been voided, in the event of any claim, he may return the product at the earliest possible convenience to the original place of purchase, together with a copy of the original document of purchase, a full description of the application the product is used on and a description of the defect. Pack the product in such a way as to ensure safe transportation (see our safety instructions).

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*Chapter*

**1**

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# Introduction

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# 1. Introduction

## 1.1 Board Overview

The CP6004X-SA (Standard Air-Cooled) is a highly integrated 6U CompactPCI system controller board based on the 3<sup>rd</sup> generation Intel® Core™ i7 processor combined with the mobile Intel® QM77 Express Chipset.

The board supports the Intel® Core™ i7-3615QE quad-core processor with 2.3 GHz built on 22-nm technology and provided in a BGA package. The processor is soldered on the CP6004X-SA which results in a higher Mean Time Between Failures (MTBF) and a significant improvement in cooling.

Two SODIMM sockets are available on the CP6004X-SA to provide up to 16 GB dual-channel, DDR3 memory with Error Checking and Correction (ECC) running at 1600 MHz. The graphics controller and the memory controller are integrated in the processor. Furthermore, either one 2.5" HDD/SSD or up to 64 GB NAND flash memory (SSD) via a SATA Flash module can be integrated onto the CP6004X-SA.

For maximum application flexibility, the CP6004X-SA comes with an extensive range of interfaces such as up to five Gigabit Ethernet ports (three on front I/O and two on rear I/O in compliance with PICMG 2.16), three high-resolution graphics interfaces (VGA, 2 x HDMI/DVI), two COM ports (RS-232 on front and rear I/O), and six SATA interfaces with RAID 0/1/5/10 functionality, one for the onboard SATA connector, one for the SATA Flash module or the 2.5" HDD/SSD, and four for rear I/O. In addition, six USB 2.0 ports are available on the board, two on front I/O and four on rear I/O.

Support for one XMC module ensures individual system expansion via the XMC interface utilizing a x8 lane PCI Express 2.0 interconnection.

The board supports a configurable 64-bit/66 MHz PCI or PCI-X hot swap CompactPCI interface. When installed in the system slot, the interface is enabled, and when installed in a peripheral slot, the CP6004X-SA is isolated from the CompactPCI bus.

In addition, the CP6004X-SA provides support for high-speed serial rear I/O interconnection via two 10GBASE-KR ports and one x4 PCI Express 2.0 port operating at up to 5 GT/s. In general, the high-speed serial rear I/O interconnection is capable of supporting two 10GBASE-KR/40GBASE-KR4 ports, one x8 PCI Express 3.0 port operating at 8 GT/s, and two SATA 6 Gb/s ports. The mechanical implementation and the signal definition of the high-speed serial rear I/O interconnection is compliant with the PICMG 2.20 specification. The CP6004X-SA is also compliant with the PICMG 2.16 specification and provides support for Kontron's latest 6U rear transition modules. The CP6004X-SA has been designed for use in a Kontron CompactPCI backplane based on the PICMG 2.20 and PICMG 2.16 specifications.

Safety and security features via a Trusted Platform Module (TPM) 1.2 are provided on request. Intelligent Platform Management Interface (IPMI) is supported as well.

Designed for stability, the board fits into applications situated in industrial environments, including I/O intensive applications where only one slot is available for the CPU, making it a perfect core technology for long-life applications. Components with high temperature tolerance have been selected from embedded technology programs, and therefore offer long-term availability.

The board is offered with various Board Support Packages including Windows, VxWorks and Linux operating systems. For further information concerning the operating systems available for the CP6004X-SA, please contact Kontron.



## 1.2 Board-Specific Information

The CP6004X-SA is a CompactPCI single-board computer based on the 3<sup>rd</sup> generation Intel® Core™ i7 processor and specifically designed for use in highly integrated platforms with solid mechanical interfacing for a wide range of industrial environment applications.

Some of the CP6004X-SA's outstanding features are:

- Support for the Intel® Core™ i7-3615QE (SV) quad-core processor, 2.3 GHz, 6 MB L3 cache
- Intel® QM77 Express Chipset
- Up to 16 GB, dual-channel, DDR3 SDRAM memory with ECC running at 1600 MHz on two SODIMM sockets
- Integrated 3D high-performance graphics controller with three high-resolution graphics interfaces (VGA, 2 x HDMI/DVI)
- 64-bit/66 MHz PCI or PCI-X CompactPCI interface (PICMG 2.0)
- One XMC slot utilizing a x8 lane PCI Express 2.0 interconnection
- Five Gigabit Ethernet interfaces:
  - Three Gigabit Ethernet interfaces on front I/O
  - Two Gigabit Ethernet interfaces on rear I/O (PICMG 2.16)
- Two Gigabit Ethernet (GbE) controllers:
  - One Intel® 82579LM Gigabit Ethernet controller connected to one GbE port on the front panel
  - One Intel® I350 quad-port Gigabit Ethernet controller connected to two GbE ports on the front panel and two GbE ports on the rear I/O
- Two 10 Gigabit Ethernet interfaces (10GBASE-KR) on the rear I/O (PICMG 2.20)
- One Intel® 82599 dual-port 10 Gigabit Ethernet controller
- Six SATA interfaces with SATA RAID 0/1/5/10 support:
  - One onboard SATA 6 Gb/s interface for the standard SATA onboard connector
  - One onboard SATA 6 Gb/s interface for either one SATA Flash module or one SATA 2.5" HDD/SSD
  - Four SATA 3 Gb/s interfaces on the rear I/O
- Six USB ports:
  - Two USB 2.0 ports on the front panel
  - Four USB 2.0 ports on the rear I/O
- Two COM ports:
  - One RS-232 COM port either on the front panel or on the rear I/O (COMA)
  - One RS-232 COM port on the rear I/O (COMB)
- TCG 1.2 compliant Trusted Platform Module (TPM), on request
- Two SPI boot flashes for two separate uEFI BIOS images:
  - One standard SPI boot flash
  - One recovery SPI boot flash
- Watchdog timer
- Battery-backed real-time clock (RTC)
- Three onboard DIP switches for board configuration
- Supports PICMG Packet Switching Backplane Specification 2.16
- IPMI support
- 4HP, 6U CompactPCI
- Passive heat sink solution for forced-airflow cooling
- Rear I/O on J3 and J5
- High-speed interconnect serial rear I/O on J4 and J41 (PICMG 2.20)
- Hot swap capability: as system controller or as peripheral device
- AMI Aptio®, a uEFI-compliant platform firmware



## 1.3 System Expansion Capabilities

### 1.3.1 XMC Module

The CP6004X-SA has one XMC mezzanine interface for support of x1, x4 and x8 PCI Express 2.0 XMC modules providing an easy and flexible way to configure the CP6004X-SA for various application requirements. For information on the XMC interface, refer to chapter 2.10.9, "XMC Interface".

### 1.3.2 CP6004X-SA-MK2.5SATA Assembly Kit

The CP6004X-SA comes with an optional CP6004X-SA-MK2.5SATA assembly kit comprised of one MMADP-SATA01 module and the necessary components needed for mounting the module on the CP6004X-SA. The MMADP-SATA01 module is required for connecting an onboard 2.5" SATA HDD or SSD to the CP6004X-SA via an onboard SATA extension connector. For further information concerning the MMADP-SATA01 module, please refer to Appendix A.

### 1.3.3 SATA Flash Module

The CP6004X-SA provides support for up to 64 GB NAND flash memory in combination with an optional SATA Flash module, which is connected to the CP6004X-SA via an onboard SATA extension connector. For further information concerning the SATA Flash module, please refer to Appendix B.

### 1.3.4 Rear I/O Module

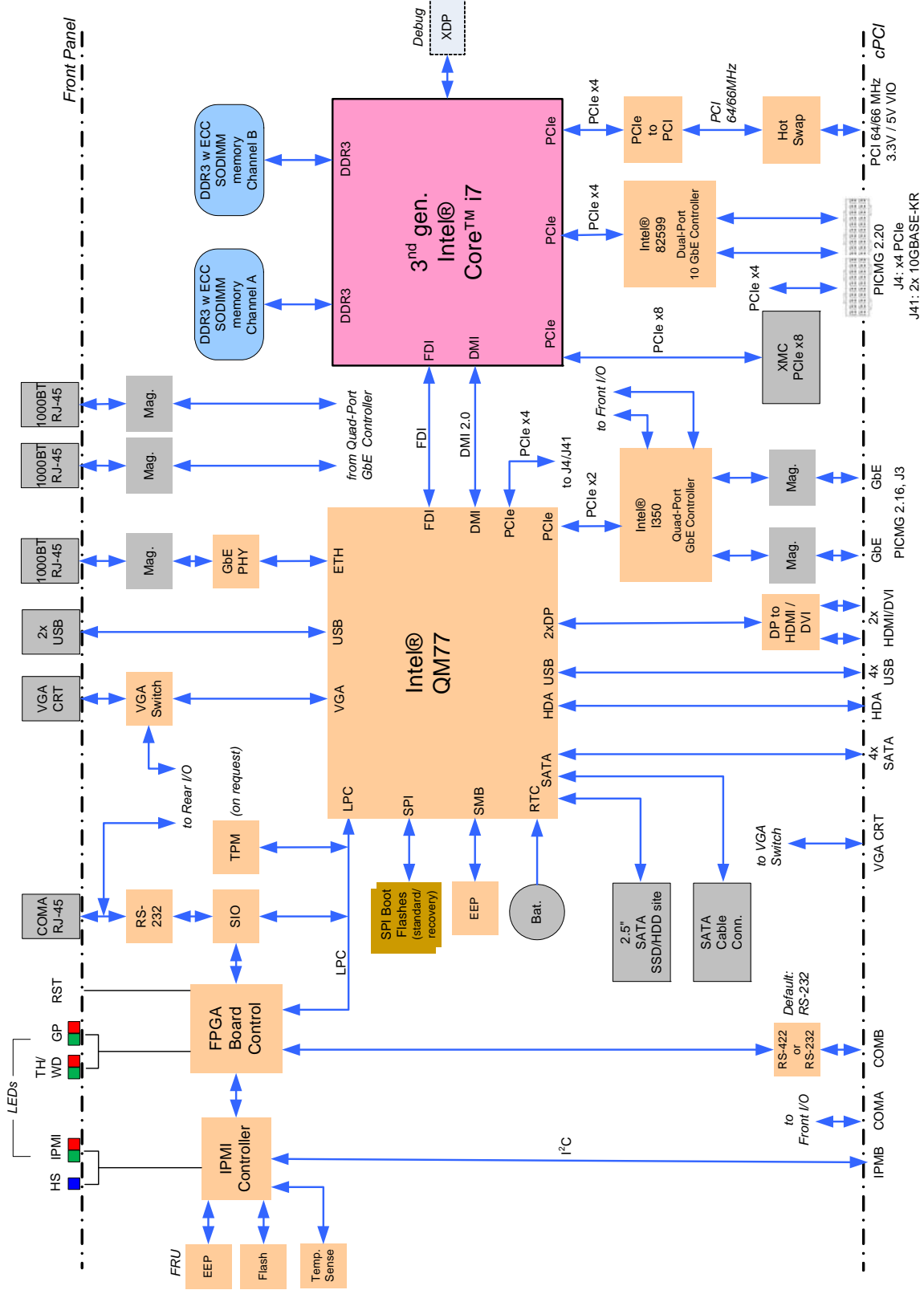
The CP6004X-SA provides support for one rear I/O module via the CompactPCI rear I/O connectors. For further information about the compatibility of rear I/O modules with the CP6004X-SA, please refer to the CP6004X-SA datasheet.

## 1.4 Board Diagrams

The following diagrams provide additional information concerning board functionality and component layout.

1.4.1 Functional Block Diagram

Figure 1-1: CP6004X-SA Functional Block Diagram

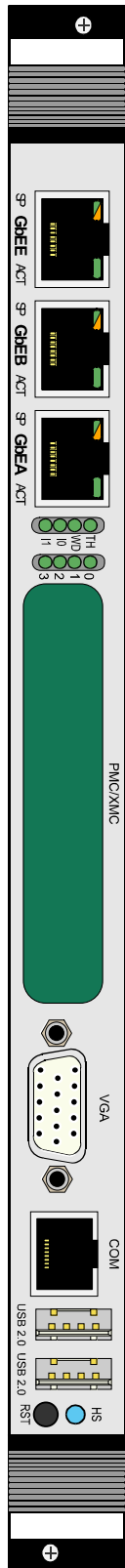






1.4.2 Front Panel

Figure 1-2: CP6004X-SA Front Panel



**Legend**

**IPMI LEDs**

I0/I1 (red/green): Indicate the software status of the IPMI controller

**Status LEDs**

WD (green): Watchdog Status

TH (red/green): Temperature Status

HS (blue): Hot Swap Control

**Integral Ethernet LEDs**

ACT (green): Ethernet Link/Activity

SPEED (green/orange/off): Ethernet Speed

**General Purpose LEDs**

LED 0..3 (red/green/amber): General Purpose/POST code



**Note ...**

If the General Purpose LEDs are lit red during boot-up, a failure is indicated before the uEFI BIOS has started.

For further information, please contact Kontron.



1.4.3 Board Layout

Figure 1-3: CP6004X-SA Board Layout – Top View

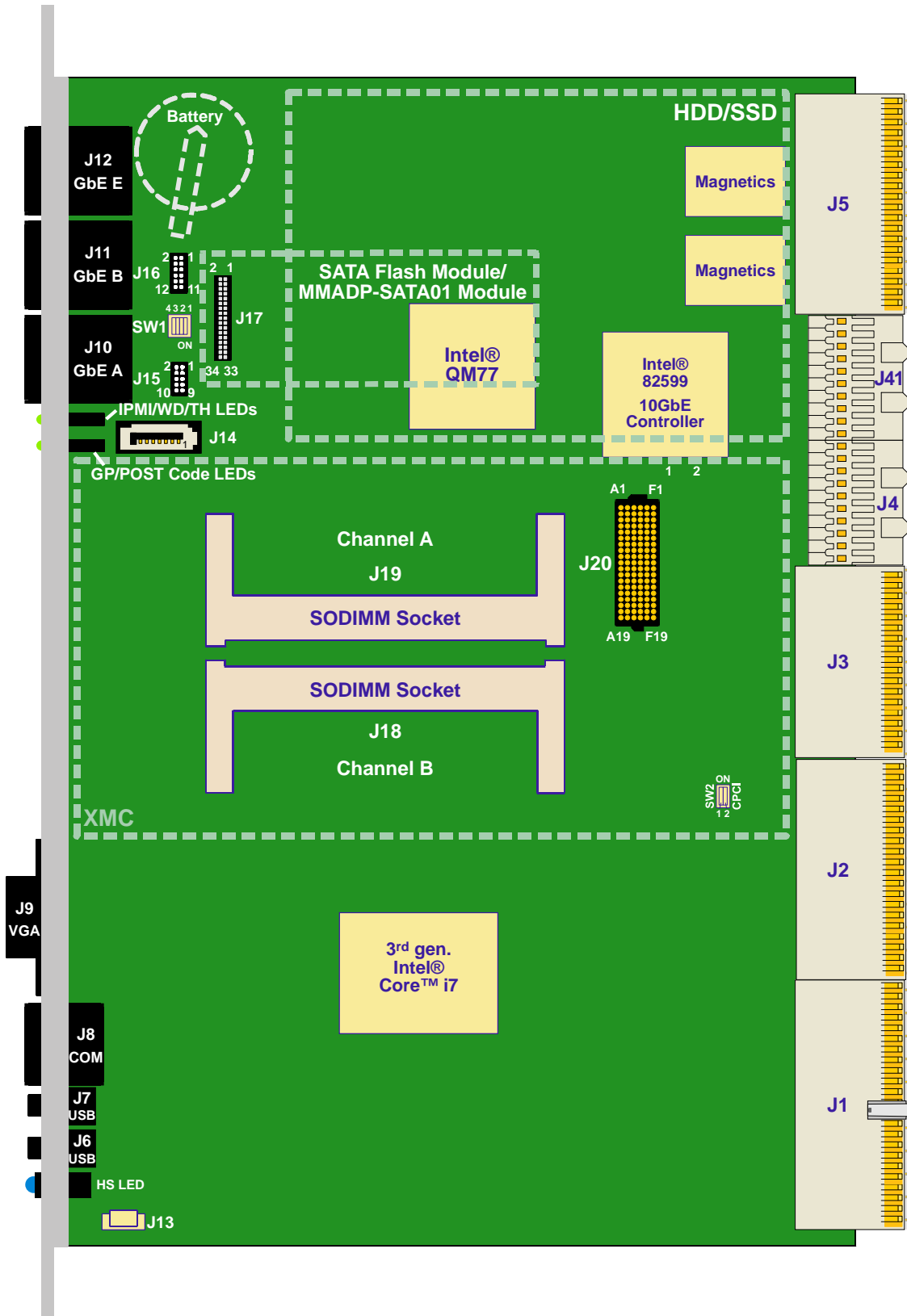
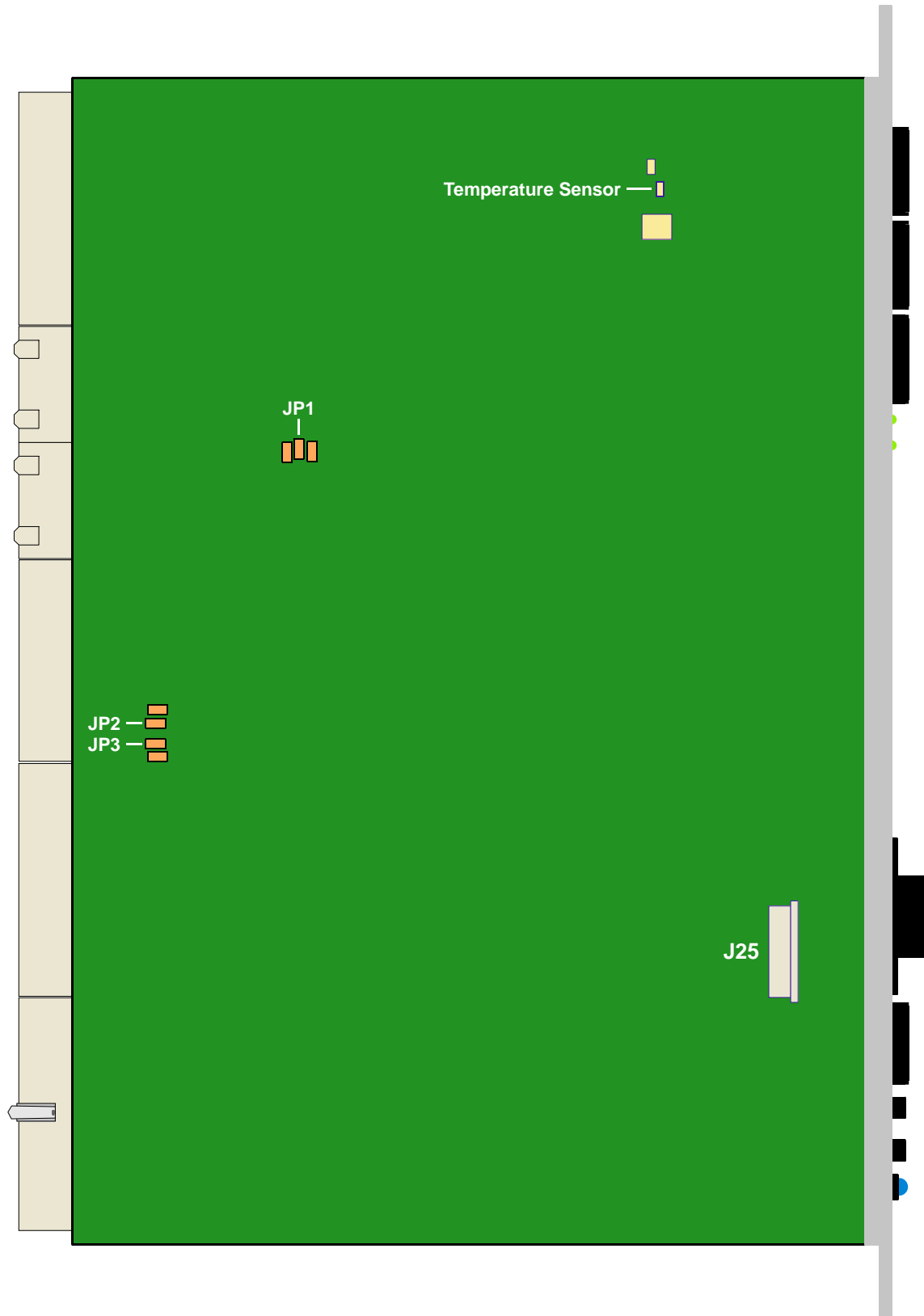




Figure 1-4: CP6004X-SA Board Layout – Bottom View



## 1.5 Technical Specification

**Table 1-1: CP6004X-SA Main Specifications**

FEATURES		SPECIFICATIONS
Processor and Memory	CPU	<p>The CP6004X-SA supports the Intel® Core™ i7-3615QE (SV) quad-core processor with 2.3 GHz and 6 MB L3 cache</p> <p>Further processor features:</p> <ul style="list-style-type: none"> <li>• Up to four execution cores</li> <li>• Intel® Hyper-Threading Technology (Intel® HT Technology)</li> <li>• Intel® 64 Architecture</li> <li>• Intel® Advanced Vector Extensions (AVX) floating point</li> <li>• Intel® Turbo Boost Technology 2.0</li> <li>• Intel® Intelligent Power Sharing (IPS)</li> <li>• System memory interface with optimized support for dual-channel DDR3 SDRAM memory at 1600 MHz with ECC</li> <li>• Integrated 2D and 3D Graphics Engines</li> <li>• DMI 2.0 with 5 GT/s and FDI interfaces to the Intel® QM77 Chipset</li> <li>• One x8 and two x4 PCI Express 2.0 ports operating at 5 GT/s</li> </ul> <p>Please contact Kontron for further information concerning the suitability of other Intel processors for use with the CP6004X-SA.</p>
	Memory	<p>Main memory:</p> <ul style="list-style-type: none"> <li>• Up to 16 GB, dual-channel DDR3 SDRAM memory with ECC running at 1600 MHz on two SODIMM sockets</li> </ul> <p>Cache structure:</p> <ul style="list-style-type: none"> <li>• 64 kB L1 cache for each core <ul style="list-style-type: none"> <li>• 32 kB instruction cache</li> <li>• 32 kB data cache</li> </ul> </li> <li>• 256 kB L2 shared instruction/data cache for each core</li> <li>• Up to 6 MB L3 shared instruction/data cache</li> </ul> <p>Flash memory:</p> <ul style="list-style-type: none"> <li>• Two SPI boot flash chips (2 x 8 MB) for two separate uEFI BIOS images</li> <li>• Up to 64 GB NAND flash via an onboard SATA Flash module (SSD)</li> </ul> <p>Serial EEPROM with 64 kbit</p>

Table 1-1: CP6004X-SA Main Specifications (Continued)

FEATURES		SPECIFICATIONS
Chipset	Intel® QM77	<p>Mobile Intel® QM77 Express Chipset:</p> <ul style="list-style-type: none"> <li>• Two x4 or eight x1 PCI Express 2.0 ports operating at 5 GT/s (only one x2 and one x4 PCI Express ports are used on the CP6004X-SA)</li> <li>• SATA host controller with six ports and RAID 0/1/5/10 support                             <ul style="list-style-type: none"> <li>• Two SATA 6 Gb/s ports accessible via onboard connectors</li> <li>• Four SATA 3 Gb/s ports accessible via rear I/O</li> </ul> </li> <li>• USB 2.0 host interface with up to 14 USB ports available (only six ports are used on the CP6004X-SA)</li> <li>• USB 3.0 host interface with up to 4 USB ports available (not used on the CP6004X-SA)</li> <li>• Integrated Ethernet controller</li> <li>• SPI flash interface support</li> <li>• Low Pin Count (LPC) interface</li> <li>• Power management logic support</li> <li>• Enhanced DMA controller, interrupt controller, and timer functions</li> <li>• System Management Bus (SMBus) compatible with most I<sup>2</sup>C™ devices</li> <li>• DMI 2.0 with 5 GT/s and FDI interfaces to the processor</li> <li>• High Definition Audio (HDA) interface</li> <li>• Analog display port</li> <li>• Three digital display ports</li> <li>• Integrated RTC</li> </ul>
Integrated Controller	Graphics controller	<p>High-performance 3D graphics controller integrated in the processor:</p> <ul style="list-style-type: none"> <li>• Supports analog displays (CRT) up to a resolution of 2048 x 1536 pixels with 32-bit color @ 75 Hz</li> <li>• Supports digital displays (HDMI/DVI) up to a resolution of 1920 x 1200 pixels @ 60 Hz</li> <li>• Dynamic Video Memory Technology (DVMT)</li> </ul>
Interfaces	CompactPCI	<p>Compliant with CompactPCI Specification PICMG 2.0 R 3.0:</p> <ul style="list-style-type: none"> <li>• System controller operation</li> <li>• 64-bit/66 MHz PCI or PCI-X master interface with dedicated PCIe-to-PCI-X bridge</li> <li>• 3.3V or 5V signaling levels (universal signaling support)</li> </ul> <p>Compliant with the Packet Switching Specification PICMG 2.16.</p> <p>The CP6004X-SA supports System Master hot swap functionality and application-dependent hot swap functionality when used in a peripheral slot.</p> <p>When used as a System Master, the CP6004X-SA supports individual clocks for each slot and the ENUM signal handling is in compliance with the PICMG 2.1 Hot Swap Specification.</p> <p>When installed in a peripheral slot, the CP6004X-SA is isolated from the CompactPCI bus. It receives power from the backplane and supports rear I/O and, if the system supports it, packet switching (in this case up to two channels of Gigabit Ethernet).</p>

Table 1-1: CP6004X-SA Main Specifications (Continued)

FEATURES		SPECIFICATIONS
Interfaces	Standard Rear I/O	<p>The following interfaces are routed to the rear I/O connectors J3 and J5.</p> <ul style="list-style-type: none"> <li>• COMA (RS-232 signaling) and COMB (RS-232 signaling); no buffer on the rear I/O module is necessary</li> <li>• 4 x USB 2.0</li> <li>• 1 x CRT VGA, 2 x HDMI/DVI</li> <li>• 1 x HDA</li> <li>• 2 x Gigabit Ethernet (compliant with PICMG 2.16, R 1.0)</li> <li>• 4 x SATA 3 Gb/s</li> <li>• 4 x GPIs and 4 GPOs (LVTTTL signaling)</li> <li>• System write protection</li> </ul>
	High-Speed Serial Rear I/O Interconnection	<p>The following interfaces are provided on the rear I/O via two ZDplus high-speed connectors, J4 and J41 (PICMG 2.20):</p> <ul style="list-style-type: none"> <li>• Two 10GBASE-KR interfaces</li> <li>• One x4 PCI Express 2.0 operating at 5 GT/s as a root complex controller only</li> </ul> <p>The port mapping of the high-speed serial rear I/O interconnection on the CP6004X-SA is capable of supporting two 10GBASE-KR/40GBASE-KR4 interfaces, one x8 PCI Express 3.0 operating at 8 GT/s, and two SATA 6 Gb/s ports. However, the current implementation provides support for only two 10GBASE-KR and one x4 PCI Express 2.0 operating at 5 GT/s.</p>
	10 Gigabit Ethernet	Two 10GBASE-KR interfaces for high-speed serial rear I/O interconnection based on the Intel® 82599 dual-port 10 Gigabit Ethernet controller
	Gigabit Ethernet	<p>Five 10 Base-T/100 Base-TX/1000 Base-T Gigabit Ethernet interfaces based on one Intel® 82579LM Gigabit Ethernet controller and one Intel® I350 quad-port Gigabit Ethernet controller:</p> <ul style="list-style-type: none"> <li>• Three RJ-45 connectors on the front panel</li> <li>• Two ports on the rear I/O (PICMG 2.16)</li> <li>• Automatic mode recognition (Auto-Negotiation)</li> <li>• Automatic cabling configuration recognition (Auto-MDI/X)</li> </ul>
	USB	<p>Six USB ports supporting UHCI (USB 1.1) and EHCI (USB 2.0):</p> <ul style="list-style-type: none"> <li>• Two type A USB 2.0 connectors on the front panel</li> <li>• Four USB 2.0 ports on the rear I/O interface</li> </ul>
	Serial	<p>Two 16C550-compatible UARTs:</p> <ul style="list-style-type: none"> <li>• One RS-232 port on the front panel and routed to rear I/O, COMA</li> <li>• One RS-232 port on the rear I/O, COMB</li> </ul>
	XMC	<p>XMC interface:</p> <ul style="list-style-type: none"> <li>• One onboard XMC connector for connecting a standard XMC module</li> <li>• Up to x8 lanes PCI Express 2.0 ports operating at 5 GT/s</li> </ul>
	SATA	<p>Two SATA 6 Gb/s interfaces for:</p> <ul style="list-style-type: none"> <li>• Up to 64 GB flash memory via an onboard SATA Flash module, or</li> <li>• Onboard 2.5" HDD/SSD is supported in combination with the MMADP-SATA01 module</li> <li>• One standard SATA 6 Gb/s interface for the standard SATA connector</li> </ul> <p>Four SATA 3 Gb/s ports accessible via rear I/O</p>



**Table 1-1: CP6004X-SA Main Specifications (Continued)**



FEATURES		SPECIFICATIONS
Sockets	Front Panel Connectors	<ul style="list-style-type: none"> <li>VGA: 15-pin, D-Sub connector, J9</li> <li>USB: two 4-pin, type A connectors, J6 and J7</li> <li>Ethernet: three 8-pin, RJ-45 connectors, J10, J11 and J12</li> <li>Serial port: one 8-pin, RJ-45 connector, J8 (COMA)</li> <li>XMC front panel bezel cutout</li> </ul>
	Onboard Connectors	<ul style="list-style-type: none"> <li>XMC connector, J20 (P15)</li> <li>Two SATA connectors                             <ul style="list-style-type: none"> <li>One 7-pin, standard SATA connector, J14</li> <li>One 34-pin, SATA extension connector, J17</li> </ul> </li> <li>JTAG connector, J16</li> <li>Debug connector, J15</li> <li>XDP-SFF (debug) connector, J25</li> <li>CompactPCI connectors J1, J2, J3 and J5</li> <li>ZDplus high-speed serial rear I/O connectors, J4 and J41 (PICMG 2.20)</li> <li>Two 204-pin DDR3 SODIMM sockets, J18 and J19</li> </ul>
Switches	DIP Switches	Two onboard DIP switches, SW1 and SW2 for board configuration
	Reset Switch	One front panel hardware reset switch
	Hot Swap Switch	One switch for hot swap purposes integrated in the front panel handle in accordance with PICMG 2.1 Rev. 2.0.
LEDs	System LEDs	System Status LEDs: <ul style="list-style-type: none"> <li>I0/I1 (red/green): Indicate the software status of the IPMI controller</li> <li>WD (green): Watchdog Status</li> <li>TH (red/green): Temperature Status</li> <li>HS (blue): Hot Swap Control</li> </ul> General Purpose LEDs: <ul style="list-style-type: none"> <li>LED 0..3 (red/green/amber): General Purpose/POST code</li> </ul>
	Ethernet LEDs	Gigabit Ethernet Status: <ul style="list-style-type: none"> <li>ACT (green): Ethernet Link/Activity</li> <li>SPEED (green/orange/off): Ethernet Speed</li> </ul>
Timer	Watchdog Timer	<ul style="list-style-type: none"> <li>Software-configurable, two-stage Watchdog with programmable timeout ranging from 125 ms to 4096 s in 16 steps</li> <li>Serves for generating IRQ or hardware reset</li> </ul>
	System Timer	<ul style="list-style-type: none"> <li>The Intel® QM77 Chipset contains three 8254-style counters which have fixed uses</li> <li>In addition to the three 8254-style counters, the Intel® QM77 Chipset includes eight individual high-precision event timers that may be used by the operating system. They are implemented as a single counter each with its own comparator and value register.</li> </ul>

Table 1-1: CP6004X-SA Main Specifications (Continued)

FEATURES		SPECIFICATIONS
IPMI	IPMI Controller	<ul style="list-style-type: none"> <li>• NXP® ARM7 microcontroller with redundant 512 kB firmware flash and automatic roll-back strategy</li> <li>• The IPMI controller carries out IPMI commands such as monitoring several onboard temperature conditions, board voltages and the power supply status, and managing hot swap operations.</li> <li>• The IPMI controller is accessible via two IPMBs (through the J1 and J2 connectors) and one host Keyboard Controller Style (KCS) Interface.</li> </ul>
Thermal	Thermal Management	<p>CPU and board overtemperature protection is provided by:</p> <ul style="list-style-type: none"> <li>• Temperature sensors integrated in the 3<sup>rd</sup> generation Intel® Core™ i7 processor: <ul style="list-style-type: none"> <li>• One temperature sensor for monitoring each processor core</li> <li>• One temperature sensor for monitoring the graphics core</li> <li>• One temperature sensor for monitoring the package die temperature</li> </ul> </li> <li>• One temperature sensor integrated in the Intel® QM77 Chipset for monitoring the chipset</li> <li>• One onboard temperature sensor for monitoring the board temperature</li> <li>• Specially designed heat sink</li> </ul>
Security	TPM	Trusted Platform Module (TPM) 1.2 for enhanced hardware- and software-based data and system security (on request)
Software	uEFI BIOS	<p>AMI Aptio®, AMI's next-generation BIOS firmware based on the uEFI Specification and the Intel Platform Innovation Framework for EFI.</p> <ul style="list-style-type: none"> <li>• LAN boot capability for diskless systems (standard PXE)</li> <li>• Redundant image; automatic fail-safe recovery in case of a damaged image</li> <li>• Non-volatile storage of setting in the SPI boot flash (battery only required for the RTC)</li> <li>• Compatibility Support Module (CSM) providing legacy BIOS compatibility based on AMIBIOS8</li> <li>• Command shell for diagnostics and configuration</li> <li>• uEFI shell commands executable from mass storage device in a Pre-OS environment (open interface)</li> <li>• IPMI support in the command shell</li> </ul>
	Software IPMI	<p>IPMI firmware providing the following features:</p> <ul style="list-style-type: none"> <li>• The IPMI controller is accessible via up to two IPMBs, IOL and one KCS interface with interrupt support</li> <li>• The IPMI firmware can be updated in the field through all supported onboard interfaces using the update functions of the open-source tool "ipmitool". For further information on the ipmitool refer to the sourceforge.net web site.</li> <li>• Two IPMI controller flash banks with automatic roll-back capability in case of an upgrade firmware failure</li> <li>• Board supervision and control extensions such as board reset, power and SPI boot flash control, etc.</li> </ul>
	Operating Systems	The board is offered with various Board Support Packages including Windows, VxWorks and Linux operating systems. For further information concerning the operating systems available for the CP6004X-SA, please contact Kontron.



Table 1-1: CP6004X-SA Main Specifications (Continued)

FEATURES		SPECIFICATIONS
General	Mechanical	6U, 4HP, CompactPCI-compliant form factor
	Power Consumption	See Chapter 5 for details.
	Temperature Ranges	Operational: 0°C to +60°C Standard Storage: -40°C to +85°C Without hard disk and without battery  <i>Note ...</i>  When a battery is installed, refer to the operational specifications of the battery as this determines the storage temperature of the CP6004X-SA (See "Battery" below).  <i>Note ...</i>  When additional components are installed, refer to their operational specifications as this will influence the operational and storage temperature of the CP6004X-SA.
	Battery	3.0V lithium battery for RTC with battery socket. Battery type: UL-approved CR2025 Temperature ranges: Operational: -20°C to +70°C typical (refer to the battery manufacturer's specifications for exact range) Storage: -55°C to +70°C typical (no discharge)
	Climatic Humidity	93% RH at 40 °C, non-condensing (acc. to IEC 60068-2-78)
	Dimensions	233.35 mm x 160 mm
	Board Weight	799 g (without mezzanine cards)

## 1.6 Standards

The board complies with the requirements of the following standards:

**Table 1-2: Standards**

TYPE	ASPECT	STANDARD	REMARKS
CE	Emission	EN55022 EN61000-6-3	--
	Immission	EN55024 EN61000-6-2	--
	Electrical Safety	EN60950-1	--
Mechanical	Mechanical Dimensions	IEEE1101.10	--
Environmental	Climatic Humidity	IEC60068-2-78	93% RH at 40°C, non-condensing (see note below)
	WEEE	Directive 2002/96/EC	Waste electrical and electronic equipment
	RoHS 2	Directive 2011/65/EU	Restriction of the use of certain hazardous substances in electrical and electronic equipment
	Vibration (Sinusoidal)	IEC60068-2-6	Ruggedized version test parameters: <ul style="list-style-type: none"> <li>• 10-300 (Hz) frequency range</li> <li>• 2 (g) acceleration</li> <li>• 1 (oct/min) sweep rate</li> <li>• 10 cycles/axis</li> <li>• 3 axes</li> </ul>
	Single Shock	IEC60068-2-27	Ruggedized version test parameters: <ul style="list-style-type: none"> <li>• 30 (g) acceleration</li> <li>• 9 (ms) shock duration half sine</li> <li>• 3 number of shocks per direction (total: 18)</li> <li>• 6 directions</li> <li>• 5 (s) recovery time</li> </ul>
	Permanent Shock	IEC60068-2-29	Ruggedized version test parameters: <ul style="list-style-type: none"> <li>• 15 (g) acceleration</li> <li>• 11 (ms) shock duration half sine</li> <li>• 500 number of shocks per direction</li> <li>• 6 directions</li> <li>• 5 (s) recovery time</li> </ul>

**Note ...**

Kontron performs comprehensive environmental testing of its products in accordance with applicable standards.

Customers desiring to perform further environmental testing of Kontron products must contact Kontron for assistance prior to performing any such testing. This is necessary, as it is possible that environmental testing can be destructive when not performed in accordance with the applicable specifications.

In particular, for example, boards **without conformal coating** must not be exposed to a change of temperature exceeding 1K/minute, averaged over a period of not more than five minutes. Otherwise, condensation may cause irreversible damage, especially when the board is powered up again.

Kontron does not accept any responsibility for damage to products resulting from destructive environmental testing.

## 1.7 Related Publications

The following publications contain information relating to this product.

**Table 1-3: Related Publications**

PRODUCT	PUBLICATION
CompactPCI Systems and Boards	CompactPCI Specification PICMG 2.0, Rev. 3.0
	CompactPCI Packet Switching Backplane Specification PICMG 2.16 Rev. 1.0
	CompactPCI Packet Serial Mesh Backplane Specification PICMG 2.20 Rev. 1.0
	CompactPCI System Management Specification PICMG 2.9 Rev. 1.0
	CompactPCI Hot Swap Specification PICMG 2.1 Rev. 2.0
	IPMI - Intelligent Platform Management Interface Specification v2.0
	Kontron CompactPCI Backplane Manual, ID 24229
SATA	Serial ATA 1.0a Specification
XMC Module	ANSI/VITA 42.0-200x XMC Switched Mezzanine Card Auxiliary Standard
	ANSI/VITA 42.3-2006 XMC PCI Express Protocol Layer Standard
	IEEE 1386-2001, IEEE Standard for a Common Mezzanine Card (CMC) Family
Platform Firmware	Unified Extensible Firmware Interface (uEFI) Specification, Version 2.1
All Kontron products	Product Safety and Implementation Guide, ID 1021-9142
Kontron	CP6004-SA/CP6004X-SA uEFI BIOS User Guide
	CP6004-SA/CP6004X-SA IPMI Firmware User Guide



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