



Errata



Manual: 14278

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1. Reference: Page 4-1, Chapter 4, Text

The referenced information (text) is revised as follows:

The following note is added:



Note ...

When resetting the CPU or an external reset of the VMOD-2 occurs, the outputs are set to "off" at power on.

2. Reference: Page 5-1, Chapter 5, Text

The referenced information (text) is revised as follows:

The following note is added:



Note ...

The AGND, BGND, CGND and DGND signals are internally connected to Analog Ground (AGND) and cannot be used as independent signals.

3. Reference: Additions to Issue 2, Table

The referenced information (table) is superseded by the following table:

Board	Version	Init. Error (max.) at 23°C % of full scale	Max. Temp. Drift 0 .. 70°C [%/°C] of full scale	Max. Temp. Drift -40 .. 85°C [%/°C] of full scale
PB-DAC3	0 - 10 Volt	< 0.15	< 0.003	< 0.004
	±10 Volt	< 0.15	< 0.003	< 0.004
	0 - 20mA	< 0.15	< 0.001	< 0.004



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