PB-DOUT2 Manual: 03086

Errata 3

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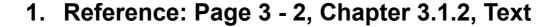
1.	Reference: Page 3 - 2, Chapter 3.1.2, Text	. 3 - 3
2.	Reference: Page 3 - 2, Chapter 3.1.3, Text	. 3 - 3
3.	Reference: Page 4 - 3, Chapter 4.2, Figure 4.2.0.1	. 3 - 3
4.	Reference: Page 4 - 5, Chapter 4.3, Figure 4.3.0.1	. 3 - 3
5.	Reference: Page 4 - 6. Chapter 4.4. Table	. 3 - 3

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The status register address offset \$00 is changed to read: \$01.

The base offset address \$00 is changed to read: \$01.

2. Reference: Page 3 - 2, Chapter 3.1.3, Text

The ID register address \$40 is changed to read: \$7F.

3. Reference: Page 4 - 3, Chapter 4.2, Figure 4.2.0.1

The table accompanying the referenced figure is changed as follows.

The signal for pin 25 is changed to read: Reset+VCC.

The signal for pin 26 is changed to read: Reset GND.

4. Reference: Page 4 - 5, Chapter 4.3, Figure 4.3.0.1

The table accompnaying the referenced figure is changed as follows.

The signal for pin 9 is changed to read: Reset Ext VCC

The signal for pin 42 is changed to read: Reset Ext GND

5. Reference: Page 4 - 6, Chapter 4.4, Table

The referenced information (table) is changed as follows.

VMOD-2, pin 26 is changed to read: 25.

VMOD-2, pin 42 is changed to read: 9.

VMOD-2, pin 25 is changed to read: 26.

VMOD-2, pin 9 is changed to read 42.

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